**Vishay Semiconductors** 

# Ultrafast Rectifier, 16 A FRED Pt®



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Cathode O Anode 2

PRODUCT SUMMARY					
Package	TO-263AC (SMPD)				
I <sub>F(AV)</sub>	16 A				
V <sub>R</sub>	600 V				
V <sub>F</sub> at I <sub>F</sub>	0.91 V				
t <sub>rr</sub>	55 ns				
T <sub>J</sub> max.	175 °C				
Diode variation	Single die				

#### FEATURES

- $\bullet$  Ultrafast recovery time, reduced  $\mathsf{Q}_{\mathsf{rr}},$  and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM, snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>solder pad</sub> = 141 °C	16	А			
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_J = 25 \ ^{\circ}C$ , 6 ms square pulse	160	A			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-	
Farming the sec	V	I <sub>F</sub> = 16 A	-	1.04	1.25	V
Forward voltage V <sub>F</sub>		I <sub>F</sub> = 16 A, T <sub>J</sub> = 150 °C	-	0.91	1.1	
		$V_{R} = V_{R}$ rated	-	-	15	
Reverse leakage current	everse leakage current I <sub>R</sub>		-	70	300	μA
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	16	-	pF

Revision: 10-Feb-15 1 Document Number: 95818 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>





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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}$	õs, V <sub>R</sub> = 30 V	-	55	-	
Reverse recovery time	+	$I_{\rm F} = 0.5 \; {\rm A}, \; I_{\rm R} = 1 \; {\rm A}, \; I_{\rm rr}$	-	-	55		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	100	-	ns
		T <sub>J</sub> = 125 °C		-	150	-	
Deck receiver aurrent		T <sub>J</sub> = 25 °C	l <sub>F</sub> = 16 A, dl <sub>F</sub> /dt = 500 A/µs,	-	20	-	^
Peak recovery current	IRRM	T <sub>J</sub> = 125 °C	$V_{\rm R} = 400 \text{ V}$	-	27	-	A
	0	T <sub>J</sub> = 25 °C	]	-	1	-	μC
Reverse recovery charge Q <sub>rr</sub>		T <sub>J</sub> = 125 °C	]	-	2	-	μΟ

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	+175	°C	
Thermal resistance, junction to solder pad	R <sub>thJ-Sp</sub>		-	1.2	1.7	°C/W	
Approximate weight				0.55		g	
				0.02		oz.	
Marking device		Case style TO-263AC (SMPD)		16EI	DU06		

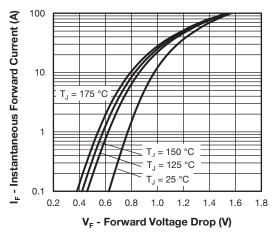


Fig. 1 - Typical Forward Voltage Drop Characteristics

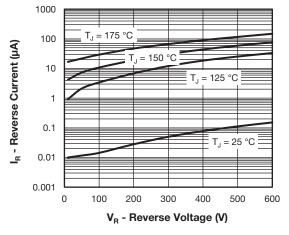


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

## VS-16EDU06-M3

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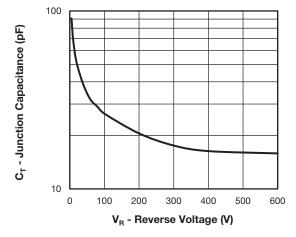


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

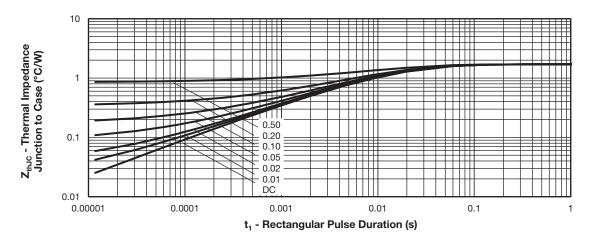
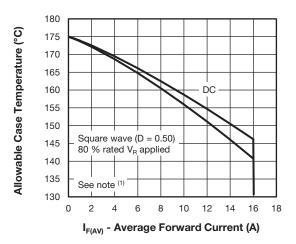
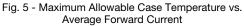
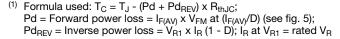


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics







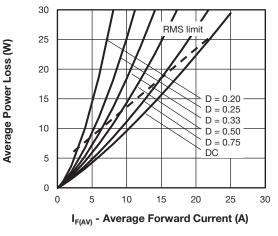


Fig. 6 - Forward Power Loss Characteristics

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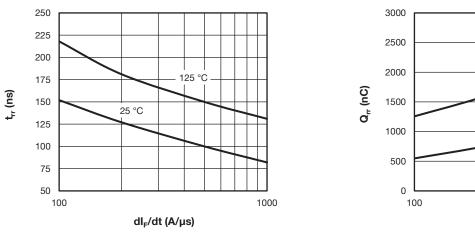


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_{\mbox{\scriptsize F}}/dt$ 

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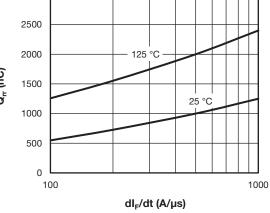


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

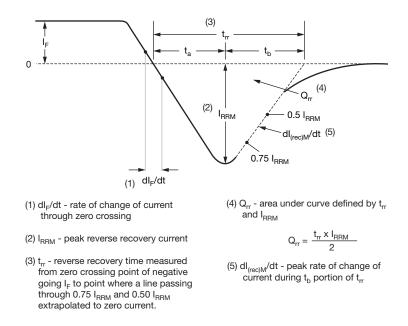


Fig. 9 - Reverse Recovery Waveform and Definitions

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### **ORDERING INFORMATION TABLE**

Device code	VS-	16	Е	D	U	06	-M3
	1	2	3	4	5	6	7
	1	- Visl	nay Sem	nicondu	ctors pr	oduct	
	2 -	- Cur	rent rati	ng (16 A	<del>\</del> )		
	3 -	- Circ	cuit conf	figuratio	n:		
		E =	single d	lie			
	4	- D =	SMPD	package	е		
	5 -	- Pro	cess typ	be,			
		U =	ultrafas	t recove	ery		
	6 -	- Vol	tage coo	de (06 =	600 V)		
	7 -	-M3	3 = halog	gen-free	e, RoHS	-compli	iant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-16EDU06-M3/I	2000	2000	13" diameter plastic tape and reel				

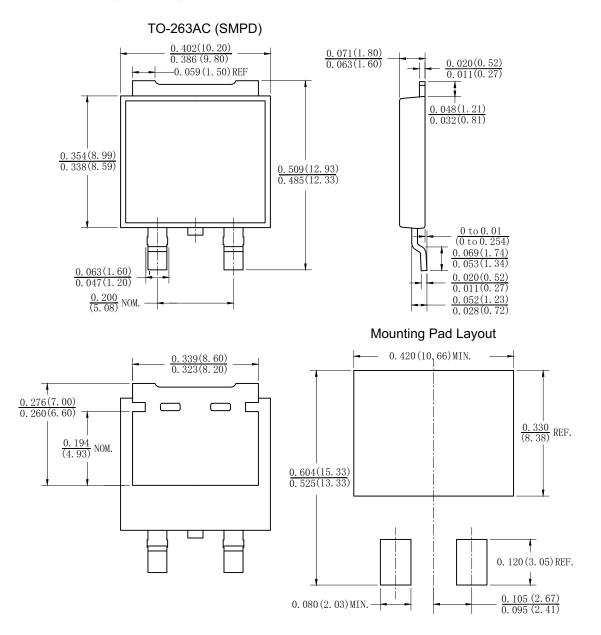
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95604					
Part marking information	www.vishay.com/doc?95566				
Packaging information	www.vishay.com/doc?88869				





TO-263AC (SMPD)

#### **DIMENSIONS** in inches (millimeters)





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