3 V SUPER MINIMOLD L-BAND SI MMIC DOWNCONVERTER

UPC2756TB

FEATURES

CEL

- HIGH DENSITY SURFACE MOUNTING: 6 Pin Super Minimold or SOT-363 package
- WIDE BAND OPERATION: RF = 0.1 to 2.0 GHz IF = 10 to 300 MHz
- ON BOARD OSCILLATOR
- SUPPLY VOLTAGE: Vcc = 2.7 TO 3.3 V

DESCRIPTION

NEC's UPC2756TB is a silicon MMIC integrated circuit manufactured using the NESAT III process. The device consists of a double balance mixer, an IF amplifier and a built-in LO. this device is suitable as a L-BAND downconverter for the receiver stage of wireless systems. The UPC2756TB is pin compatible and has comparable performance as the larger UPC2756T, so it is suitable for use as a replacement to help reduce system size. The IC housed in a 6 pin super minimold or SOT-363 package.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

ELECTRICAL CHARACTERISTICS (TA = 25° C, Vcc = 3 V, ZL = Zs = 50Ω)

| | PART NUMBER PACKAGE OUTLINE | UPC2756TB \$06 | | | |
|-----------|---|-------------------|------------|------------|----------|
| SYMBOLS | PARAMETERS AND CONDITIONS | UNITS | MIN | ТҮР | MAX |
| Icc | Circuit Current (no signal) | mA | 3.5 | 6.0 | 8.0 |
| fRF | RF Frequency Response (3 dB down from the gain at $f_{RF} = 900$ MHz, $f_{IF} = 150$ MHz) | GHz | 0.1 | | 2.0 |
| fı⊧ | IF Frequency Response (3 dB down from the gain at f_{RF} = 900 MHz, f_{IF} = 150 MHz) | MHz | 10 | 300 | |
| CG | Conversion Gain ¹ frF = 900 MHz, frF = 150 MHz frF = 1.6 GHz, frF = 20 MHz | dB dB | 11 11 | 14 14 | 17 17 |
| NF | Noise Figure frF = 900 MHz, fiF = 150 MHz frF = 1.6 GHz, fiF = 20 MHz | dB dB | | 10 13 | 13 16 |
| PSAT | Saturated Output Power ² fRF = 900 MHz, fiF = 150 MHz fRF = 1.6 GHz, fiF = 20 MHz | dBm dBm | -11 -15 | -8 -12 | |
| OIP3 | SSB Output 3rd Order Intercept Point fRF = 0.8~2.0 GHz, fIF = 100 MHz | dBm | | +4 | |
| ISO | LO Leakage, fLO = 0.8 ~2.0 GHz at RF pin at IF pin | dBm dBm | | -35 -23 | |
| PN | Phase Noise ³ , fosc = 1.9 GHz | dBc/Hz | | -68 | |
| Rth (J-A) | Thermal Resistance (Junction to Ambient) Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB | °C/W | | | 325 |

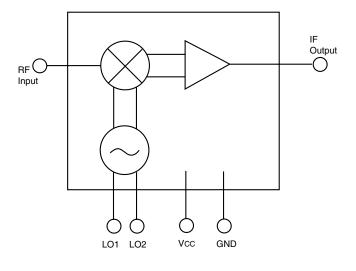
Notes:

1. PRF = -40 dBm.

2. PRF = -10 dBm.

3. See Application Circuit.

INTERNAL BLOCK DIAGRAM



California Eastern Laboratories

ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

| SYMBOLS | PARAMETERS | UNITS | RATINGS |
|---------|--------------------------------------|-------|-------------|
| Vcc | Supply Voltage | V | 5.5 |
| Рт | Total Power Dissipation ² | mW | 200 |
| Тор | Operating Temperature | °C | -40 to +85 |
| Tstg | Storage Temperature | °C | -55 to +150 |

Notes:

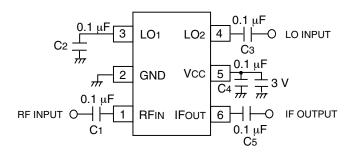
1. Operation in excess of any one of these parameters may result in permanent damage.

2. Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB (TA = $+85^{\circ}$ C).

RECOMMENDED OPERATING CONDITIONS

| SYMBOLS | PARAMETERS | UNITS | MIN | ТҮР | MAX |
|---------|-----------------------|-------|-----|-----|-----|
| Vcc | Supply Voltage | V | 2.7 | 3.0 | 3.3 |
| Тор | Operating Temperature | °C | -40 | +25 | +85 |

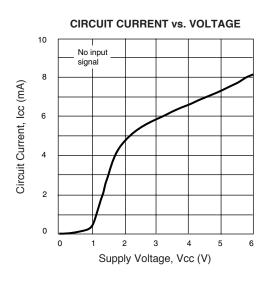
TEST CIRCUIT



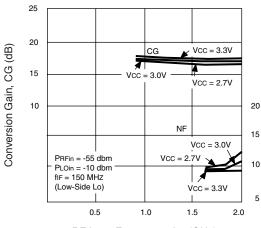
PIN FUNCTIONS

| Pin No. | Symbol | Applied Voltage (V) | Pin Voltage (V) | Description | Internal Equivalent Circuit |
|---------|--------|---------------------|-----------------|--|-----------------------------|
| 1 | RFin | _ | 1.2 | Signal input pin to double balancec mixer. This pin must be coupled to the signal source with a blocking capacitor. | |
| 2 | GND | 0 | _ | Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. | |
| 3 | LO1 | _ | 1.2 | These pins are both the base- collectors of a differential amplifier configured to oscillate when equipped with an external tank resonator circuit. Each pin must be coupled to the tank circuit with a blocking capacitor. In the case of an external LO source, bypass the unused pin with a capacitor to ground. | |
| 4 | LO2 | - | 1.2 | | <i></i> |
| 5 | Vcc | 2.7 to 3.3 | - | Power supply pin. This pin should be externally equipped with a bypass capacitor to minimize ground impedance. | |
| 6 | ΙΕουτ | _ | 1.7 | Output of single-ended push-pull IF buffer amplifier. This is an emitter-follower output with low impedance. This pin must be coupled to the next stage with a blocking capacitor. | Vcc 6 |

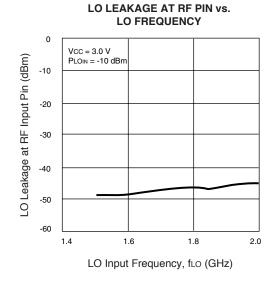
TYPICAL PERFORMANCE CURVES (TA = 25°C)

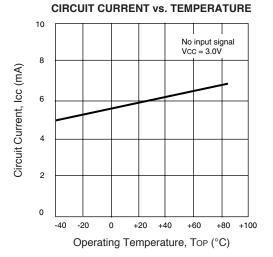


CONVERSION GAIN AND NOISE FIGURE vs. RF INPUT FREQUENCY

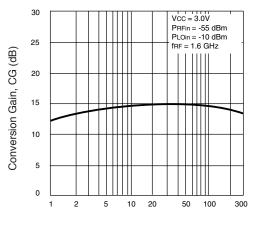


RF Input Frequency, fRF (GHz)





CONVERSION GAIN vs. IF OUTPUT FREQUENCY



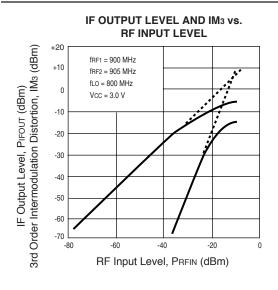
IF Output Frequency, fiF (GHz)

LO LEAKAGE AT IF PIN vs. LO FREQUENCY 0 VCC = 3.0V PLOIN = -10 dBm -10 -20 -30 -40 -50 -60 0.8 1.0 1.2 1.4 1.6

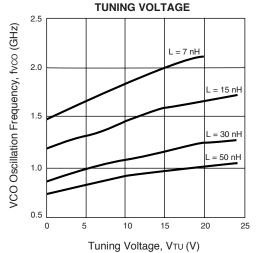
LO Leakage at IF Output Pin (dBm)

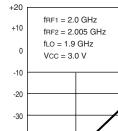


TYPICAL PERFORMANCE CURVES (TA = 25°C)



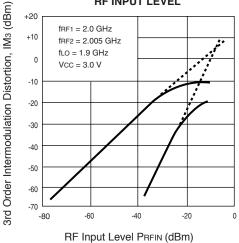
VCO OSCILLATION FREQUENCY vs.



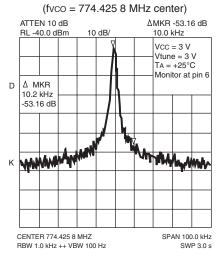


IF Output Level, PIFOUT (dBm)

IF OUTPUT LEVEL AND IM3 vs. **RF INPUT LEVEL**

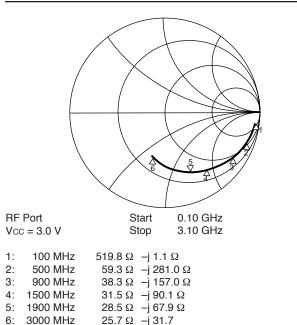


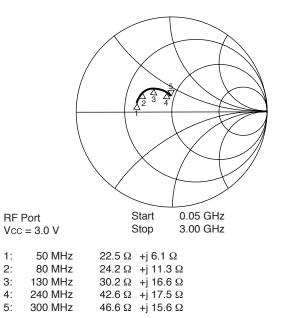
VCO PHASE NOISE



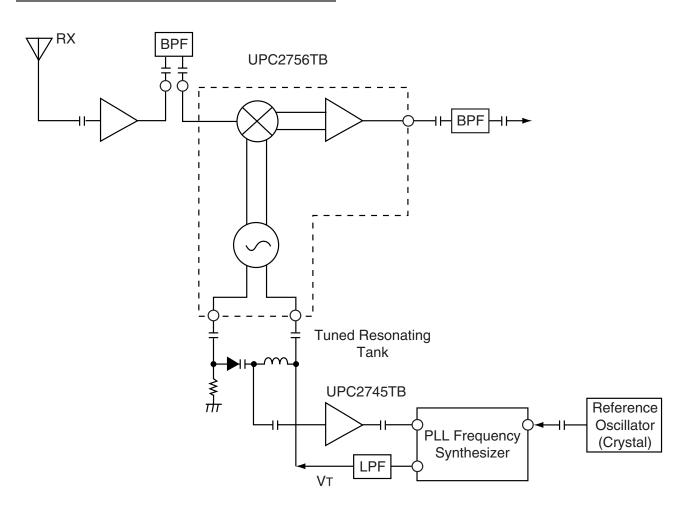
VCO PHASE NOISE (fvco = 1.639 194 2 MHz center) ATTEN 10 dB ΔMKR -40.34 dB RL -40.0 dBm 10 dB/ 10.2 kHz VCC = 3 V VCC = 3 VVtune = 3 V TA = +25°C Monitor at pin 6 ∆ mkr D 10.2 kHz -40.34 dB When when Κ CENTER 1.639 194 2 GHZ SPAN 100.0 kHz RBW 1.0 kHz ++ VBW 100 Hz SWP 3.0 s

TYPICAL SCATTERING PARAMETERS



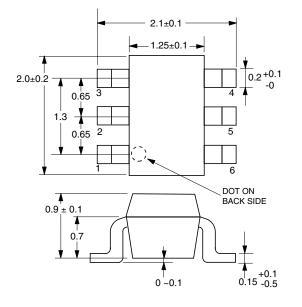


SYSTEM APPLICATION EXAMPLE

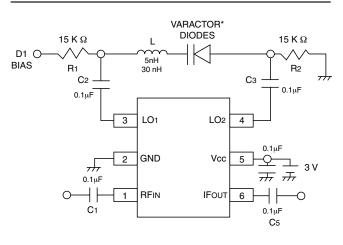


OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S06



APPLICATION CIRCUIT EXAMPLE



* Recommended Varactor Diodes:

Alpha SMV1204-4, Toshiba 1SV186 or equivalent

Life Support Applications

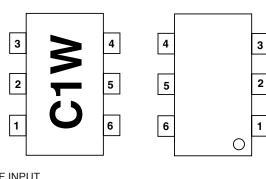
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LEAD CONNECTIONS

(Top View)

(Bottom View)



1. RF INPUT 2. GND 3. LO₁ 4. LO₂ 5. VCC 6. IF OUTPUT

| PART NUMBER | QTY |
|----------------|---------|
| UPC2756TB-E3-A | 3K/Reel |

Note:

Embossed Tape, 8 mm wide, Pins 1, 2, 3 are in tape pull-out direction.



Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

| Restricted Substance per RoHS | Concentration Limit per RoHS (values are not yet fixed) | Concentration contained in CEL devices | | |
|----------------------------------|--|---|------------|--|
| Lead (Pb) | < 1000 PPM | -A Not Detected | -AZ (*) | |
| Mercury | < 1000 PPM | Not Detected | | |
| Cadmium | < 100 PPM | Not Detected | | |
| Hexavalent Chromium | < 1000 PPM | Not Detected | | |
| РВВ | < 1000 PPM | Not Detected | | |
| PBDE | < 1000 PPM | Not Detected | | |

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